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FDLC Meeting and Conference

October 17th, 2012

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Federal Government Information
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What is a patent?



A Patent is a property right granted by the United States to the original inventor(s) for a limited time in exchange for public disclosure of the invention.

The Patent gives the inventor the [negative] right “to exclude others from making, using, offering for sale, or selling” the invention in the United States, or importing the invention into the United States.

Three types of patents

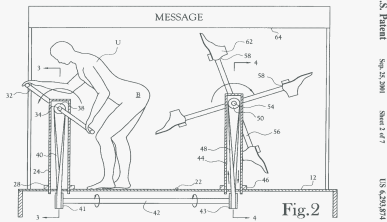
- **Utility Patents** – Granted for a process; machine or manufacture; composition of matter; or an improvement thereof. Utility patents have sequential numbers.
- **Design Patents** – Protects the new, ornamental design (i.e. “outward appearance”) for an article of manufacture. Design patent numbers are preceded by the letter “D.”
- **Plant Patents** – granted on any distinct and new variety of an asexually reproduced plant. Plant patent numbers are preceded by “PP.”

Patent Protection Terms

- Utility patent – twenty years from non-provisional patent filing date
- Design patent – fourteen years from the issue date
- Plant patent – twenty years from filing date

Once these terms expire, the invention is now 'public domain' *and anyone may now use it without licensing!*

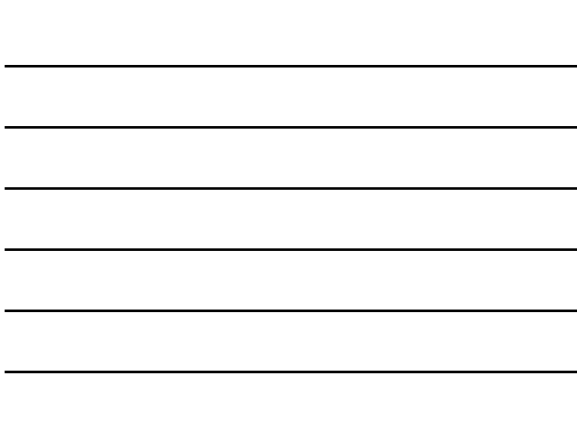
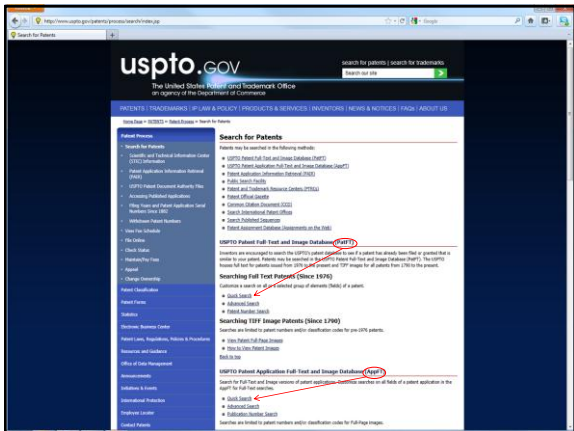
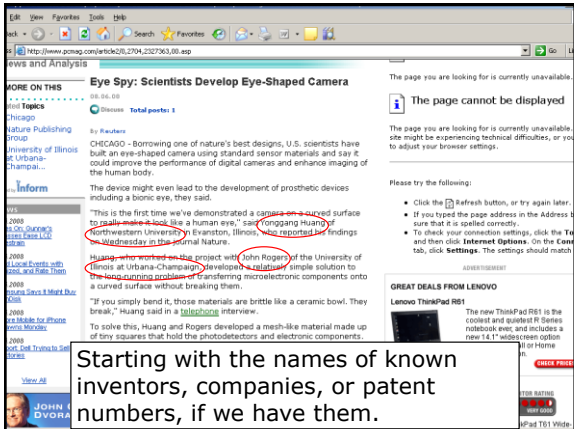
**Please Note! –
Obtaining a patent does not
guarantee commercial success!**



How do we start?

From the most specific to the more general:

- Start your search from a known piece of information – a patent number, inventor name, company or university. Look at inventions that are similar to yours, and look at their classifications for similar inventions.
- Search the patent databases using likely keywords or combinations, and examine the resulting 'hits' for similarity; then look at the classifications on the most similar patents.
- Use the Patent Classification tools – <http://www.uspto.gov/patents/resources/classification/index.jsp>



USPTO PATENT FULL-TEXT AND IMAGE DATABASE

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Note truncation mark! (\$) Data current through January 31, 2012.

Query [Help]

Term 1: Ni-Huang-Yonggang in Field 1: Inventor Name

AND

Term 2: in Field 2: All Fields

Select years [Help] 1976 to present (full-text)

Search Reset

Patents from 1790 through 1975 are searchable only by Issue Date, Patent Number, and Current US Classification. When searching for specific numbers in the Patent Number field, patent numbers must be seven characters in length, excluding commas, which are optional.

Patents may be searched using text from 1976 to the present. Patents that issued before 1976 may be retrieved by Issue Date, Patent Number or Current US Classification, and then viewed as page images in TIFF format, with all drawings and text.

USPTO PATENT FULL-TEXT AND IMAGE DATABASE

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Searching US Patent Collections...

Results of Search in US Patent Collections
 Ni-Huang-Yonggang: 4 patents.
 Hits: 1 through 4 out of 4

Jump To: []

Refine Search: Ni-Huang-Yonggang

PAT. NO.	Title
7,866,133	Characterizing curvatures and stresses in thin-film structures on substrates having spatially non-uniform variations
7,930,113	Mechanism stresses in multi-layer thin film systems with variable film thickness
7,487,050	Techniques and devices for characterizing spatially non-uniform curvatures and stresses in thin-film structures on substrates with non-local effects
7,363,173	Techniques for analyzing non-uniform curvatures and stresses in thin-film structures on substrates with non-local effects

Top View Cart

Home Quick Advanced Pat Num Help

None of these granted patents look like the invention mentioned in the article, so maybe it hasn't been granted yet. Let's leave the Granted Patents file, and go into the Patent Applications file.

To change the file we're searching, we first click on the 'Home' button..

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 An Agency of the Department of Commerce

Patent Full-Text Databases

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US Patent & Trademark Office
PATENT APPLICATION FULL TEXT AND IMAGE DATABASE

Data current through February 2, 2012.

Query [\[Help\]](#)

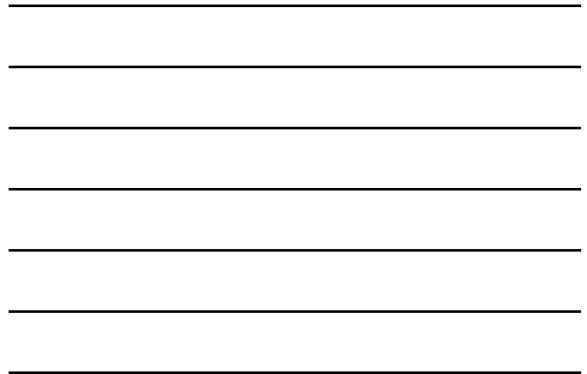
Term 1: **Huang-Yonggang** in Field 1: **Inventor Name**

AND

Term 2: in Field 2: All Fields

Select years [\[Help\]](#) **2001-present** Search Reset

The USPTO agreed to begin publishing patent applications eighteen months after their filing date in 1999, so the first published applications in this file are from March 2001.



US Patent & Trademark Office
PATENT APPLICATION FULL TEXT AND IMAGE DATABASE

Searching AppFT Database...

Results of Search in AppFT Database for:
IN:Huang-Yonggang; 6 applications.
Hits 1 through 6 out of 6

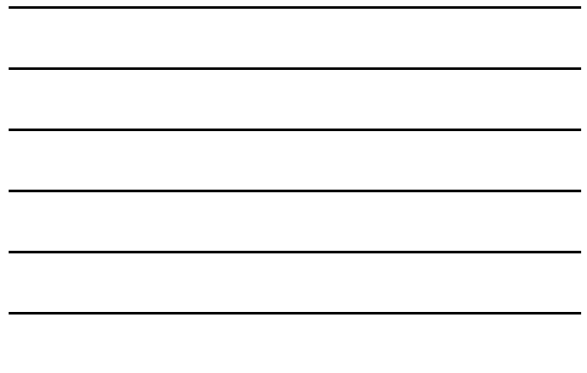
Jump To:

Refine Search: IN/Huang-Yonggang

PUB. APP. NO.	Title
1 20110240747	IMPLANTABLE BIOMEDICAL DEVICES ON BIORESORBABLE SUBSTRATES
2 20100007407	Stretchable and Foldable Electronic Devices
3 20080127235	CONTROLLED BUCKLING STRUCTURES IN SEMICONDUCTOR INTERCONNECTS AND NANOMEMBRANES FOR STRETCHABLE ELECTRONICS
4 20070189919	Characterizing Curvatures and Stresses in Thin-Film Structures on Substrates having Spatially Non-Uniform Variations
5 20060276977	Techniques and devices for characterizing spatially non-uniform curvatures and stresses in thin-film structures on substrates with non-local effects
6 20050278126	Techniques for analyzing non-uniform curvatures and stresses in thin-film structures on substrates with non-local effects

The first three applications had not been granted patents by January 2012, when I captured this screen.

No. 3 is the patent from the original news article, and no. 2 might be a more recent (2010) technical advancement by the same inventor!



US Patent & Trademark Office
PATENT APPLICATION FULL TEXT AND IMAGE DATABASE

Full images of every page of the granted patent or published applications, but your computer must be able to display TIFF graphic files from within the web browser.

United States Patent Application 20080127235
Kind Code A1
Rogers; John A.; et al. July 3, 2008

CONTROLLED BUCKLING STRUCTURES IN SEMICONDUCTOR INTERCONNECTS AND NANOMEMBRANES FOR STRETCHABLE ELECTRONICS

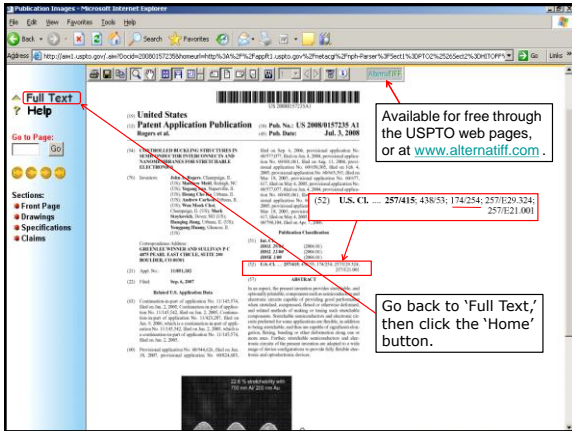
Abstract

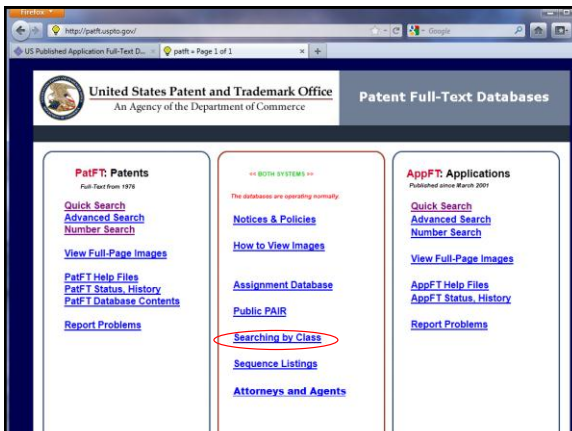
In an aspect, the present invention provides stretchable, and optionally printable, components such as semiconductors and electronic circuits capable of providing good performance when stretched, compressed, flexed or otherwise deformed, and related methods of making or tuning such stretchable components. Stretchable semiconductors and electronic circuits preferred for some applications are flexible, in addition to being stretchable, and thus are capable of significant elongation, flexing, bending or other deformation along one or more axes. Further, stretchable semiconductors and electronic circuits of the present invention are adapted to a wide range of device configurations to provide fully flexible electronic and optoelectronic devices.

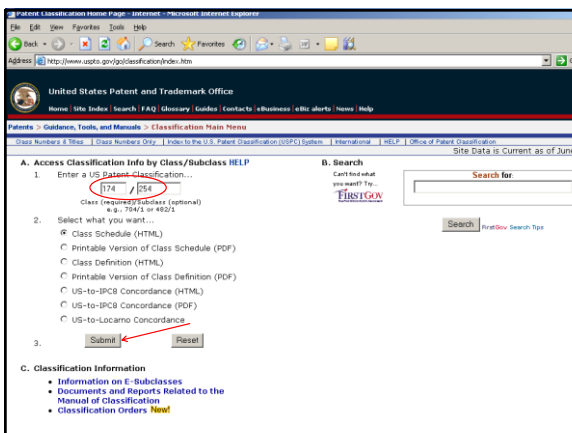
Inventors: Rogers; John A.; (Champaign, IL); Moss; Matthew; (Raleigh, NC); Sun; Yugang; (Naperville, IL); Ko; Heung Cho; (Urbana, IL); Carlson; Andrew; (Urbana, IL); Choi; Won Moak; (Champaign, IL); Stoykovich; Mark; (Dover, NH); Jiang; Haoyang; (Urbana, IL); Huang; Yonggang; (Glencoe, IL)

Correspondence Address: GREENLEE WINNER AND SULLIVAN P.C. 4875 PEARL EAST CIRCLE, SUITE 200 BOULDER, CO









Class **174** ELECTRICITY: CONDUCTORS AND INSULATORS
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Select Largest Indent Level to be Displayed | Select Expansion Level | Submit

- 1 MISCELLANEOUS
- 2 LIGHTNING PROTECTION
- 4R AIR TERMINALS
- 5R ELECTRIC SHOCK HAZARD PROTECTIVE DEVICES
- 6 EARTH GROUNDS
- 8 WITH FLUIDS OR VACUUM
- 32 ANTI-INDUCTIVE STRUCTURES
- 37 UNDERGROUND
- 48R OVERHEAD
- 46 HANDLES
- 480 COMBINED FLUID CONDUIT AND ELECTRICAL CONDUCTOR
- WALL MOUNTED
- 50 BOXES AND HOUSINGS
- 650 FEEDTHROUGH OR BUSHING
- COVERS OR FACE PLATES
- 66 COVERS OR FACE PLATES
- 68.1 CONDUITS, CABLES OR CONDUCTORS
- 68.2 - Bus bars or bus ducts (Residual)
- 68.3 - Single duct conduits
- 250 - Prefabricated panel circuit arrangement (e.g., printed circuit)
- 251 - With encapsulated wire
- 252 - With cooling means
- 75R Micropanels
- 254 - Convertible shape (e.g., flexible) or circuit (e.g., breadboard)

Clicking the plus sign will open the hierarchy of classifications, showing the dotted indentations.

Mainline

One-dot indentation

Two-dot indentation



United States Patent and Trademark Office
 Patents > Guidance, Tools, and Manuals > Classification > Class Schedule

Class **174** ELECTRICITY: CONDUCTORS AND INSULATORS
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- 1 MISCELLANEOUS
- 2 LIGHTNING PROTECTION
- 4R AIR TERMINALS
- 5R ELECTRIC SHOCK HAZARD PROTECTIVE DEVICES
- 6 EARTH GROUNDS
- 8 WITH FLUIDS OR VACUUM
- 32 ANTI-INDUCTIVE STRUCTURES
- 37 UNDERGROUND
- 48R OVERHEAD
- 46 HANDLES
- 480 COMBINED FLUID CONDUIT AND ELECTRICAL CONDUCTOR
- WALL MOUNTED
- 50 BOXES AND HOUSINGS
- 650 FEEDTHROUGH OR BUSHING
- 66 COVERS OR FACE PLATES
- 68.1 CONDUITS, CABLES OR CONDUCTORS
- 68.2 - Bus bars or bus ducts (Residual)
- 68.3 - Single duct conduits
- 69 - Extensible
- 250 - Prefabricated panel circuit arrangement (e.g., printed circuit)
- 251 - With encapsulated wire
- 252 - With cooling means
- 75R Micropanels
- 254 - Convertible shape (e.g., flexible) or circuit (e.g., breadboard)

Click on the Class number to open up the 'Class Definitions.' These will explain what forms of technology are placed within this class and each subclass.



Class **174** ELECTRICITY: CONDUCTORS AND INSULATORS
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SECTION I - CLASS DEFINITION

1. This class is for inventions relating to the structure of electrical conductors and insulators and the apparatus specialized to mount, support, encasing in conduits, and/or housing the same.
2. Conductors may be bare or be encased in insulation, may be single strand or plural strand, may be of single conductor form or there may be plurality of conductors associated together to form a cable.
3. Since all materials that have the property of being conductors of electricity and all devices made therefrom may be termed electrical conductors, those structures that are specially designed to conduct electricity as their proximate purpose are placed in this class.
4. Insulators are placed here when the structure thereof is claimed, which structure is specially designed for spacing two or more devices of different electrical potential from each other or for spacing one or more devices from ground.
5. Since all materials which are poor conductors of electricity and devices made therefrom may be termed electrical insulators, only those structures whose proximate purpose is that stated in the preceding paragraph are placed in this class.
6. Conduits are placed in this class only when some characteristic is claimed which limits the same to the electrical use. For reasons above stated, fact, claimed or unclaimed, that the conduit is made of electrically conductive and/or insulative material, will not cause classification in this class; systems and components are classified elsewhere. See References to Other Classes, below.

SECTION II - LINES WITH OTHER CLASSES AND WITHIN THIS CLASS

1. Housings (boxes, receptacles, containers, etc.) are placed in this class only when limited to electrical use. The mere fact, claimed or unclaimed, that the material of which the box or housing is composed is conductive, and/or insulative, will not cause classification in this class. (See D, Housing, Receptacles, below). To be classified in this class, the claims must include some structure which limits the box or housings to electrical use. The mere fact that the box or housings include an electrical device, recited by name only, is sufficient to cause classification in this class even though no other structure which limits the box or housings to electrical use. This class includes as boxes, housings or envelopes such as are used for electric lamp envelopes, electric space discharge devices, and similar electrical devices which are enclosed in vitreous, ceramic, nonmetallic plastic or metallic housings or envelopes. Where such subject matter is otherwise properly within the scope of this class, the mere naming of the type of lamp or discharge device being the device within the housing or envelope will not exclude the patent from this class. Neither will the recitation of an electrode broadly recited within the housing or envelope exclude the patent from this class although if the electrode is claimed as a filament, anode, grid or other specific



Class Definition for Class 174 - ELECTRIC...

SECTION III - SUBCLASS REFERENCES TO THE CURRENT CLASS

SEE OR SEARCH THIS CLASS, SUBCLASS:

8+, where the envelope is provided with means peculiarly adapted for use in connection with a vacuum, gas or fluid (but no gas filling where no structure peculiarly adapted for use with a vacuum or fluid except the hermetic seals is claimed.

9, where the structure includes a current conductive fluid (e.g., a liquid used as a part of the lead-in structure) or where continuously evacuated to reduce leakage of air or gas).

15.1, where the envelope or housing has combined therewith means for feeding, circulating or distributing a fluid including means therein where the cooling means involves the use of a fluid.

17+, for the miscellaneous envelopes, boxes and housings under subclass 8.

17.05+, for the miscellaneous hermetically sealed envelopes and housing within the definition of subclass 8 (including housings which form the hermetic seal).

31+, for structures under subclass 8 which are bushings or other devices for insulating a conductor or object from a wall or plate and indented subclasses for underground installations.

37, and indented subclasses for other boxes and housings, and the notes thereunder.

50, and indented subclasses for other boxes and housings, and the notes thereunder.

50.S.1, for the miscellaneous hermetically sealed envelopes and housings in this class, including those provided with lead-in wire the housing.

137, see the notes to this subclass, and the subclasses thereunder for devices for electrically insulating one or more conductive structures or ground.

151+, for devices (e.g., bushings) for insulating a conductor from a wall or plate (such as a metallic envelope wall) through which to pass a fluid.

350, through 397, for such envelopes and housings provided with an electric shield which wholly or partially surrounds the envelope or housing or a part thereof from electromagnetic or electrostatic effects.

480, through 507, for wall-mounted housings.

SECTION IV - REFERENCES TO OTHER CLASSES

SEE OR SEARCH CLASS:

Class Definition for Class 174 - ELECTRIC...

SECTION IV - REFERENCES TO OTHER CLASSES

SEE OR SEARCH CLASS:

19, Textiles: Fiber Preparation, (see Lines With Other Classes, "Miscellaneous," above.)

26, Textiles: Cloth Finishing, (see Lines With Other Classes, "Miscellaneous," above.)

28, Textiles: Manufacturing, (see Lines With Other Classes, "Miscellaneous," above.)

29, Metal Working, for processes and apparatus for making wire and filaments regardless of the material used. Included are processes and apparatus for drawing, rolling, or otherwise reducing the diameter of wire or filament, and processes and apparatus for drawing, rolling, or otherwise reducing the diameter of conductor strands, and subclasses 527.1+ where a coating operation is involved. (See Lines With Other Classes, "Miscellaneous.")

52, Static Structures (e.g., Buildings), subclass 40 for a residual mast or tower with an article support structure, having no electrical structure thereon.

52, Static Structures (e.g., Buildings), subclasses 220.1+ for a service duct within a barrier wherein a feature limited to electrical structures is claimed.

57, Textiles: Spinning, Twisting, and Tearing, takes processes and/or apparatus for making conductors by operations within the apparatus and/or process is also claimed. Patents claiming only conductor structure are in this class (174), Class 57 taking precedence over other classes.

66, Textiles: Knitting, (see Lines With Other Classes "Miscellaneous," above.)

72, Metal Deforming, takes a process or an apparatus for making or reshaping a wire by a mere plastic metal working operation.

87, Textiles: Braiding, Netting, and Lace Making, (see Lines With Other Classes, "Miscellaneous," above.)

104, Railways, subclasses 149, and indented subclasses. (Lines With Other Classes, "Conduits").

106, Compositions: Coating or Plastic, appropriate subclasses for compounds and compositions which are dielectrics, and their use in electrical apparatus.

118, Coating Apparatus, takes apparatus for coating conductors. (Lines With Other Classes, "Miscellaneous").

136, Batteries: Thermoelectric and Photoelectric, (see Lines With Other Classes, "Conductors and Electrical Systems").

138, Pipes and Tubular Conduits, for conduit structures even though claimed as electrical conduits and/or claimed as made of electrical material. (See Lines With Other Classes, "Conduits," above.)

139, Textiles: Weaving, (see Lines With Other Classes, "Miscellaneous," above.)

156, Adhesive Bonding and Miscellaneous Chemical Manufacture, subclasses 47+ takes methods of making indefinite length electrical conductors.

178, Telegraphy, subclass 45 for wave transmission systems having loaded cable structures. (See Lines With Other Classes, "Conduits and Electrical Systems").

187, Elevator, Industrial Lift Truck, or Stationary Lift for Vehicle, subclasses 277+ for an electrical control system for an elevator or industrial lift truck, or stationary lift for vehicle. (See Lines With Other Classes, "Conduits and Electrical Systems").

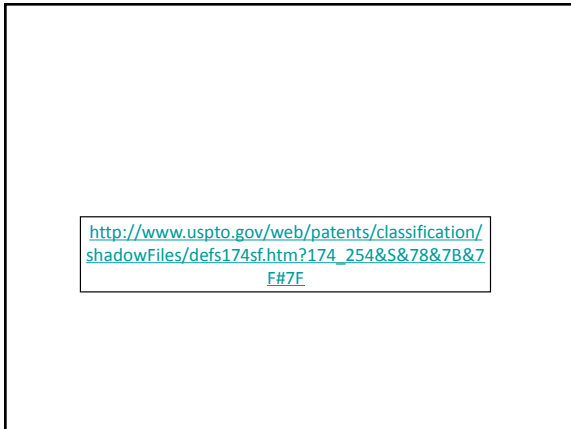
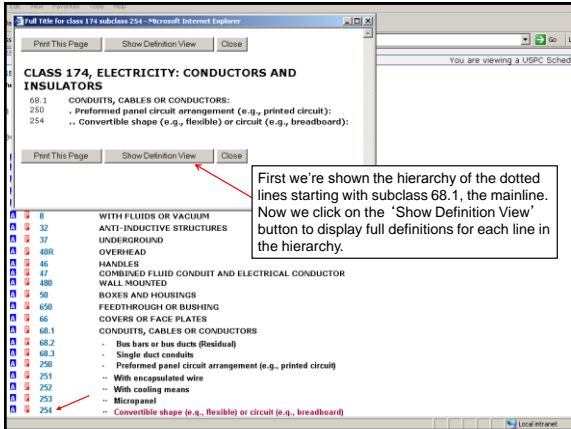
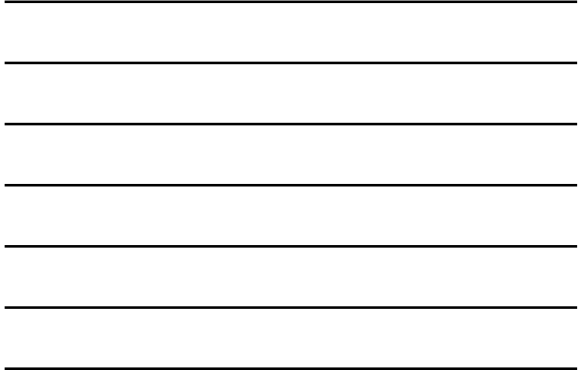
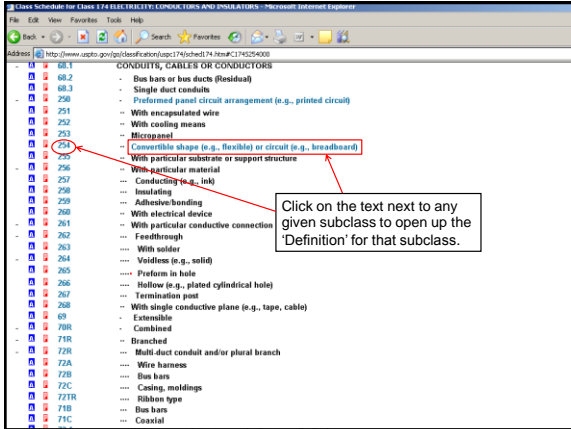
Class 174 ELECTRICITY: CONDUCTORS AND INSULATORS

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Select Largest Indent Level to be Displayed Contract To Mainlines

- 1 MISCELLANEOUS
- 2 LIGHTNING PROTECTION
- 4R AIR TERMINALS
- 5R ELECTRIC SHOCK HAZARD PROTECTIVE DEVICES
- 6 EARTH GROUNDS
- 8 WITH FLUIDS OR VACUUM
- 32 ANTI-INDUCTIVE STRUCTURES
- 37 UNDERGROUND
- 40R OVERHEAD
- 46 HANDLES
- 47 COMBINED FLUID CONDUIT AND ELECTRICAL CONDUCTOR
- 480 WALL MOUNTED
- 50 BOXES AND HOUSINGS
- 600 FEETTHROUGH OR BUSHING
- 66 COVERS OR FACE PLATES
- 68.1 CONDUITS, CABLES OR CONDUCTORS
 - 68.2 - Disks bars or bus ducts (residual)
 - 68.3 - Single duct conduits
 - 250 - Prefabricated panel circuit arrangement (e.g., printed circuit)
 - 251 - With encapsulated wire
 - 252 - With cooling means
 - 253 - Micropanel
 - 254 - Convertible shape (e.g., flexible) or circuit (e.g., breadboard)
 - 255 - With particular substrate or support structure
 - 256 - With particular material
 - 260 - With electrical device
 - 261 - With particular conductive connection (e.g., crossover)
 - 263 - With single conductive plate (e.g., tape, cable)
 - 265 - Expressions

Relevant levels of indentation. Each line should contain about 200 patents, or it's broken down further!



Class Definition for Class 174 - ELECTRIC...

254 **Convertible shape (e.g., flexible) or circuit (e.g., breadboard):**
 This subclass is indented under subclass 250. Subject matter wherein the structure is either easily bent without breaking or has means to easily change its conductor circuit configuration.
 (1) Note. Terms that are somewhat synonymous with "breadboard" are "prototype" and "universal board".

SEE OR SEARCH CLASS:
 361, Electricity: Electrical Systems and Devices, subclass 398 for flexible printed circuits which include plural, diverse electrical devices.

255 **With particular substrate or support structure:**
 This subclass is indented under subclass 250. Subject matter including a material means distinguished by significant construction or configuration which provides a supporting surface for other materials, especially materials used as printed-circuits patterns.

256 **With particular materials:**
 This subclass is indented under subclass 250. Subject matter wherein at least a part of the circuit board structure is composed of one or more specific substances.

257 **Conducting (e.g., ink):**
 This subclass is indented under subclass 256. Subject matter including a material adapted to the transmission of electricity.
 (1) Note. The conducting material may be for example superconducting, semiconducting or resistive.

258 **Insulating:**
 This subclass is indented under subclass 256. Subject matter including a material on or through which essentially no electrical current will flow.

259 **Adhesive/bonding:**
 This subclass is indented under subclass 256. Subject matter including a material which causes parts of the structure to stick, bind or fasten together.

SEE OR SEARCH THIS CLASS, SUBCLASS:
 263, for soldered feed through connections where the composition of the solder is nominal.



Class 174 ELECTRICITY: CONDUCTORS AND INSULATORS

Each of these subclass lines with a '+' has additional lines with one or more dots contracted underneath them.

When we would like to examine all patents and/or applications within a given classification, we may click on the 'P' and 'A' icons to retrieve them all.

- 2 MISCELLANEOUS
- 4R LIGHTNING PROTECTION
- 5R AIR TERMINALS
- 6R ELECTRIC SHOCK HAZARD PROTECTIVE DEVICES
- 8 EARTH GROUNDS
- 32 WITH FLUIDS OR VACUUM
- 37 ANTI-INDUCTIVE STRUCTURES
- 48R UNDERGROUND
- 46 OVERHEAD
- 47 HANDLES
- 480 COMBINED FLUID CONDUIT AND E-WALL MOUNTED
- 50 BOXES AND HOUSINGS
- 680 FEEDTHROUGH OR BUSHING
- 66 COVERS OR FACE PLATES
- 68.1 CONDUITS, CABLES OR CONDUCTORS
 - 68.2 - Bus bars or bus ducts (Residual)
 - 68.3 - Single-bus conductors
 - 250 - Performed panel circuit arrangement (e.g., printed circuit)
 - 251 - With encapsulated wire
 - 252 - With cooling means
 - 253 - Micropanel
 - 254 - Convertible shape (e.g., flexible) or circuit (e.g., breadboard)



Patent Database Search Results CCL174...

Results of Search in US Patent Collection db for:
 CCL174254: 1140 patents.
 Hits: 1 through 50 out of 1140

Here are all the granted patents in class/subclass 174/254 back to 1790:
 Feb 2010 – 992 patents;
 Feb 2011 – 1059 patents;
 Feb 2012 – 1140 patents;
 Note the increasing rate of change!

PAT. NO	Title
8,107,245	Flexible device, flexible pressure sensor
8,106,409	Optical semiconductor device with flexible substrate
8,106,309	Flexible printed circuit, display device including the same, and manufacturing method thereof
8,106,307	Substrate structure and electronic apparatus
8,106,304	Membrane structure of electronic component
8,101,861	Printed circuit board
8,097,817	Flexible pixel array substrate and flexible display
8,093,508	Flexible thin image-sensing module with anti-EMI function and flexible thin PCB module with anti-EMI function
8,093,507	Flex-rigid wiring board and manufacturing method thereof
8,089,771	Printed circuit board and semiconductor memory module using the same
8,080,726	Non-planar microcircuit structure and method of fabricating same



Lists of 'Assignees' (companies that own patents) are useful tools for marketing and analysis. This useful form of patent display was created with the freepatentsonline.com website and a spreadsheet program.

Document	Publication Date	Title	Inventor Name	Assignee
6210288	2009-08-18	Flexible printed circuit and display device	Chen, Guo-Jiang; Orita, Kazuo	Hitachi, Ltd. (Tokyo, JP)
8148842	2012-04-03	Printed circuit board and method for fabricating	Delley, Bertram	Continental Automotive GmbH (Stuttgart, DE)
7542929	2009-06-02	Optimized mounting area circuit module	Wittory, Jr., James Douglas	Electron Technologies, LP (Austin, TX, US)
5116071	1994-05-24	Line branching printed circuit board for CT	Teramichi, Akira	Mitsubishi Denki, Ltd. (Osaka, JP)
6551823	1999-07-25	Flexible circuit terminals for surface mount	Jacques, Roland G (and MA)	Flex Technology Inc. (Houston, TX, US)
6200882	2011-09-23	Print circuit substrate and connection	Morita, Hiroe	Saikoro, Ltd. (Tokyo, JP)
7651707	2010-12-14	Circuit board and method for manufacturing	Murata, Daisuke	Kawasaki IRI, Ltd. (Kawasaki, JP)
7035338	2011-03-29	Multi-layer wiring board and its manufacturing	Muramatsu, Masamitsu	Kawasaki IRI, Ltd. (Kawasaki, JP)
3267482	1996-08-18	Programmable matrix	John, Humphries; Pierce, Rose	HEWLETT PACKARD CO. (Palo Alto, CA, US)
6400580	2011-08-30	Flexible printed circuit board	Hsu, Shih-Juan; Tseng, Hsien	Hsin Hai Precision Industry Co., Ltd. (Ta-Cheng, New Taipei, TW)
6313205	2011-09-06	Printed circuit board with high density	Shiozaki, Tetsuo; Hase, Hiroshi	Hsin Hai Precision Industry Co., Ltd. (Ta-Cheng, New Taipei, TW)
6922269	2011-09-20	Flexible printed circuit board	Pai, Yi-cheng	Hsin Hai Precision Industry Co., Ltd. (Ta-Cheng, New Taipei, TW)
7170389	2010-06-31	Flexible printed wiring board	Yasuda, Kiyohisa	Hitachi, Ltd. (Tokyo, JP)
7432319	2008-09-09	Flex-rigid wiring board	Kawaguchi, Katsuo	Hitachi, Ltd. (Tokyo, JP)
7650889	2010-02-02	Flex-rigid wiring board	Kawaguchi, Katsuo	Hitachi, Ltd. (Tokyo, JP)
7382136	2011-07-19	Flex rigid wiring board and method of ma	Takahashi, Michimasa	Hitachi, Ltd. (Tokyo, JP)
8186311	2012-05-29	***WIREBOARD PATENT AS PER THE	Takahashi, Michimasa	Hitachi, Ltd. (Tokyo, JP)
7550441	2009-04-28	Cable having translucent, semi-transparent	Ichio, E. T. (Santa Clara)	International Business Machines Corporation (Armonk, NY, US)
7501101	2007-11-27	Double-sided flexible printed circuits	Mura, Kazuo	Hitachi, Ltd. (Tokyo, JP)
7255263	2007-01-17	Circuit board having signal lines adapti	Haga, Yasuhiro	Hitachi, Ltd. (Tokyo, JP)
7072737	2011-07-12	Multi-layer flexible printed circuit boar	Lee, Heung Wook	Shinshu G. I. (Seoul, KR)
5172725	1998-07-07	Flexible circuit board having improved	Yoshida, Shingo	Mitsubishi Denki Kabushiki Kaisha (Tokyo, JP)
5827328	1995-02-07	Configurable circuit substrate	Austin, Michael M. (Pompano Beach)	Motorola, Inc. (Schaumburg, IL)
8142483	1998-05-03	Methods and techniques for fabricating	Solomon, Richard L	Beauregard Security Systems, Inc. (Capezio, NY)
8115103	2012-02-14	Flexible printed board, electronic appar	Okuyama, Mayumi	Saia, Inc. (Tokyo, JP)
8119181	2012-02-21	Printed circuit board and semiconductor	Sato, Junya	Wipac Corporation (Tokyo, JP)
7079053	2010-03-16	Carrier tape	Suzuki, Yuzo	Hitachi, Ltd. (Tokyo, JP)
5147743	1998-05-05	Cool-shaped flexible printed circuit boar	Kato, Miki	Hitachi, Ltd. (Tokyo, JP)
6207081	2009-03-19	Wired circuit board and producing meth	Yasuda, Kiyohisa	Hitachi, Ltd. (Tokyo, JP)
7531753	2009-05-12	Suspension board with circuit	Funada, Yasuhito	Hitachi, Ltd. (Tokyo, JP)
6208288	2009-09-08	Printed circuit board	Ito, Jun	Hitachi, Ltd. (Tokyo, JP)
7489144	2010-01-19	Connection structure between wired cir	Miyata, Osamu	Hitachi, Ltd. (Tokyo, JP)
7109599	2010-10-19	Wired circuit board	Katsuka, Kouji	Hitachi, Ltd. (Tokyo, JP)

Looking through the patents and applications we retrieve by classification searching, we often see new keyword phrases or word combinations to use for an additional search.

132 7,701,724 Method of manufacturing a wiring substrate and semiconductor device

133 7,701,721 Flexible printed wiring board

134 7,697,301 Apparatus and method for enhancing conductivity

135 7,696,441 Flexible wired circuit board

136 7,696,002 Method for manufacturing an electronic module in an installation base

137 7,692,103 Electronic circuit device

138 7,690,105 Technique for reducing wasted material on a printed circuit board panel

139 7,688,594 Flexible printed circuit board

140 7,679,001 Carrier tape

141 7,674,984 Wiring board

142 7,667,141 Flexible printed circuit layout and method thereof

143 7,663,065 Flexible circuit board

144 7,660,126 Flexible printed circuit board

145 7,658,001 Electrical connector for disk drive suspension assembly and method of its construction

146 7,655,869 Flex rigid wiring board

147 7,652,892 Wired circuit board

148 7,649,742 Wiring substrate, semiconductor device, and method of manufacturing the same

149 7,649,731 Power distribution module wiring bus bar

150 7,649,144 Connection structure between wired circuit boards

USPTO PATENT FULL-TEXT AND IMAGE DATABASE

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Data current through January 31, 2012.

Query [Help]

Term 1: "flexible circuit board*" in Field 1: All Fields

AND

Term 2: in Field 2: All Fields

Select years [Help]

1976 to present (full-text)

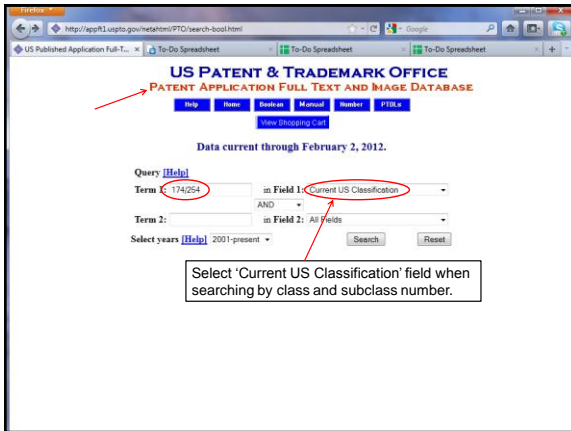
Search [Reset]

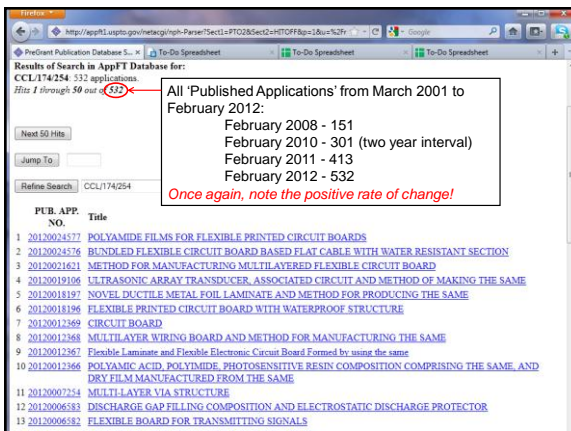
Patents from 1790 through 1975 are searchable only by Issue Date, Patent Number, and Current US Classification. When searching for specific numbers in the Patent Number field, patent numbers must be seven characters in length, excluding commas, which are optional.

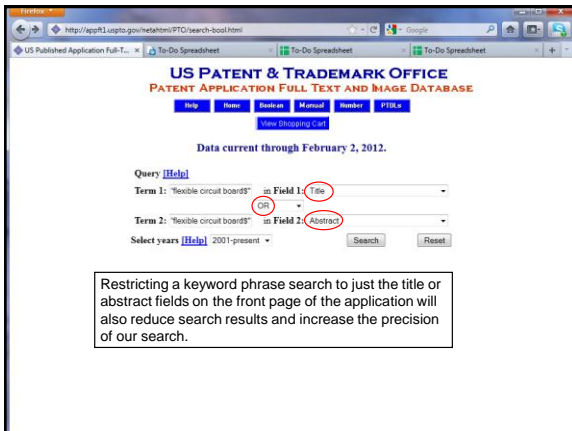
Notice the truncation mark(\$) at the end of a word, to capture plurals and variants in usage. Using quotation marks around the phrase means the exact phrase must appear in each retrieved document.

Make Sure to Search in Both the
Granted Patents (PatFT) and Published
Applications (AppFT) Databases!

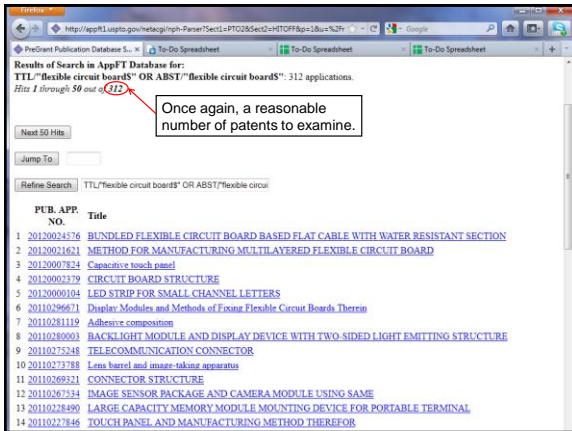
- Applications that have been published often have different claims from the patents that are eventually granted
- There are published applications that were never granted, but are now *prior art* and may not be granted to another inventor



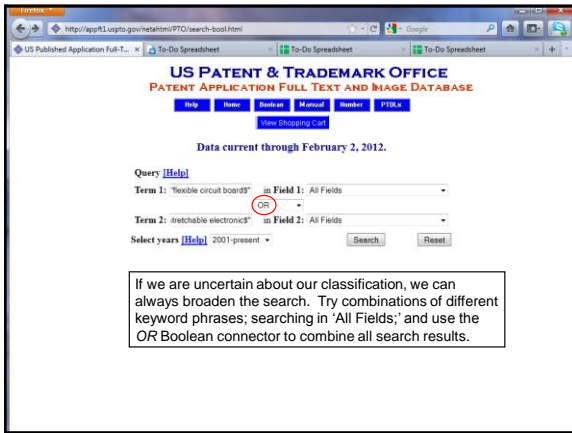




Restricting a keyword phrase search to just the title or abstract fields on the front page of the application will also reduce search results and increase the precision of our search.



Once again, a reasonable number of patents to examine.



If we are uncertain about our classification, we can always broaden the search. Try combinations of different keyword phrases; searching in 'All Fields;' and use the OR Boolean connector to combine all search results.

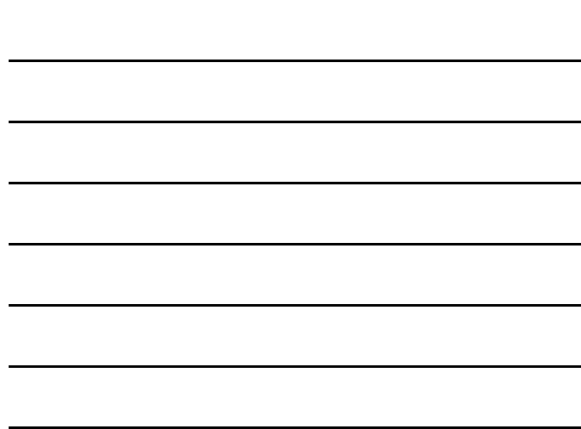


Results of Search in AppFT Database for:
 "flexible circuit boards" OR "stretchable electronics": 3307 applications.
 Hits 1 through 59 out of 3307

Next 50 Hits
 Jump To

Refine Search: "flexible circuit boards" OR "stretchable electronics"

PUB. APP. NO.	Title
1 20120029289	Optical Cap for Use With Arthroscopic System
2 20120029280	Arthroscopic System
3 20120026424	LIQUID CRYSTAL DISPLAY DEVICE INCLUDING BACKLIGHT UNIT
4 20120027836	POWER SUPPLY DEVICE FOR DETECTING DISCONNECTION OF VOLTAGE DETECTION LINES
5 20120025705	LED BULB
6 20120025499	LIQUID ENCAPSULATING RESIN COMPOSITION, SEMICONDUCTOR DEVICE USING LIQUID ENCAPSULATING RESIN COMPOSITION, AND METHOD OF MANUFACTURING THE SAME
7 20120024576	BUNDLED FLEXIBLE CIRCUIT BOARD BASED FLAT CABLE WITH WATER RESISTANT SECTION
8 20120023783	Wireless remote controlled massaging footwear system
9 20120016661	REPAIR STRUCTURE AND METHOD FOR LIQUID CRYSTAL DISPLAY
10 20120021621	METHOD FOR MANUFACTURING MULTILAYERED FLEXIBLE CIRCUIT BOARD
11 20120021234	LOW-THERMAL-EXPANSION BLOCK POLYIMIDE, PRECURSOR THEREOF, AND USE THEREOF
12 20120020055	BACKLIGHT MODULE
13 20120020011	ELECTRONIC DEVICE AND CONNECTION MECHANISM FOR TOUCH BUTTONS THEREOF



Results of Search in AppFT Database for:
 ABST,TTL("flexible circuit boards" OR "stretchable electronics"): 332 applications.
 Hits 1 through 59 out of 332

Next 50 Hits
 Jump To

Refine Search: ABST,TTL("flexible circuit boards" OR "stretchable electronics")

PUB. APP. NO.	Title
1 20120024576	BUNDLED FLEXIBLE CIRCUIT BOARD BASED FLAT CABLE WITH WATER RESISTANT SECTION
2 20120021621	METHOD FOR MANUFACTURING MULTILAYERED FLEXIBLE CIRCUIT BOARD
3 20120007824	Capacitive touch panel
4 20120003770	CIRCUIT BOARD STRUCTURE
5 20120000104	LED STRIP FOR SMALL CHANNEL LETTERS
6 20110296671	Display Modules and Methods of Fixing Flexible Circuit Boards Therein
7 20110281119	Adhesive composition
8 20110280003	BACKLIGHT MODULE AND DISPLAY DEVICE WITH TWO-SIDED LIGHT EMITTING STRUCTURE
9 20110275248	TELECOMMUNICATION CONNECTOR
10 20110273788	Lens barrel and image-taking apparatus
11 20110269321	CONNECTOR STRUCTURE
12 20110267334	IMAGE SENSOR PACKAGE AND CAMERA MODULE USING SAME
13 20110228490	LARGE CAPACITY MEMORY MODULE MOUNTING DEVICE FOR PORTABLE TERMINAL
14 20110227846	TOUCH PANEL AND MANUFACTURING METHOD THEREFOR



US PATENT & TRADEMARK OFFICE
 PATENT APPLICATION FULL TEXT AND IMAGE DATABASE

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Data current through February 2, 2012.

Query [Help](#)

Term 1: "flexible circuit boards" in Field 1: All Fields

AND

Term 2: prosthe\$ in Field 2: All Fields

Select year: [Help](#) 2001-present Search Reset

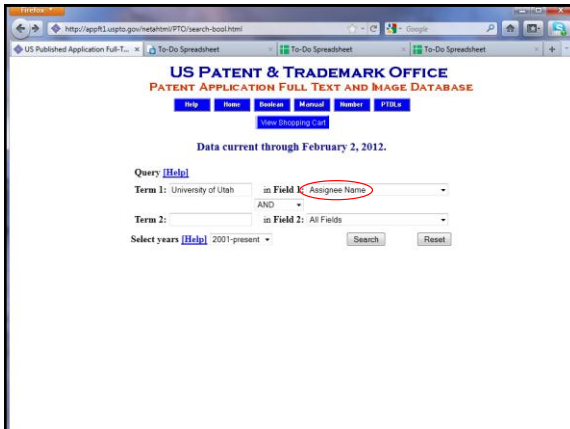
Another example of alternative keyword searches, in this case using a keyword stem and the truncation symbol –
 prosthe\$
 prosthe\$is
 prosthe\$ic etc.

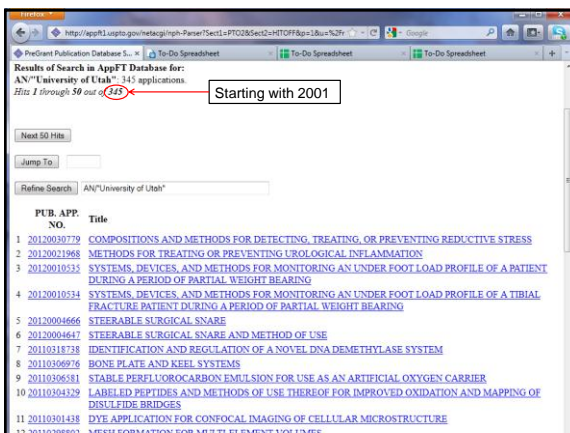


Some Basic Competitive Intelligence

We may use the patent databases to identify some of the research interests, strengths and faculty of a particular institution or research facility.

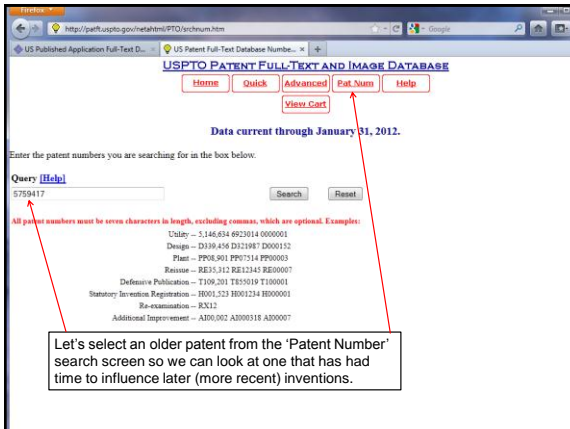
For example, let's look at published patent applications 'assigned to' (i.e., 'owned by') the University of Utah.

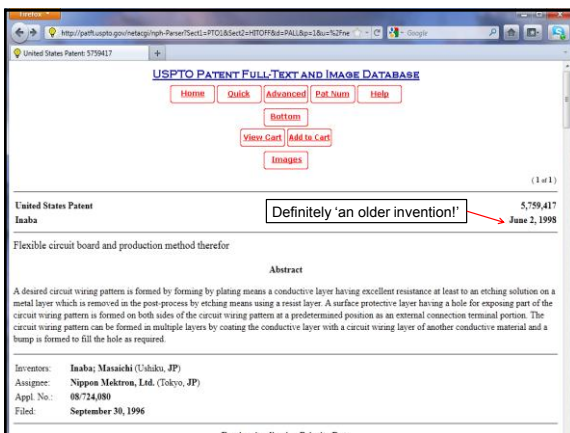


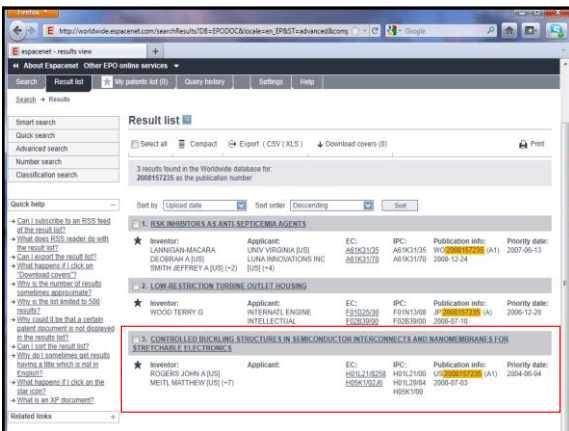
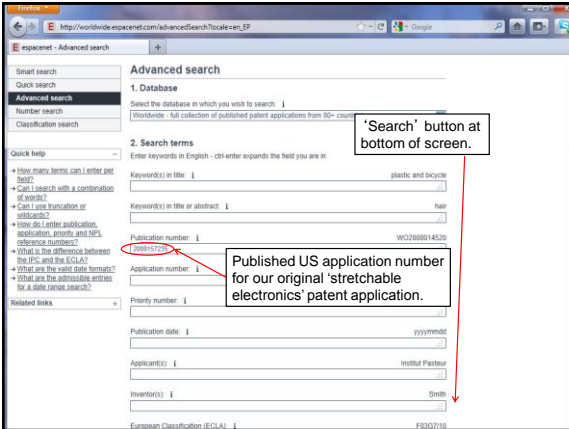
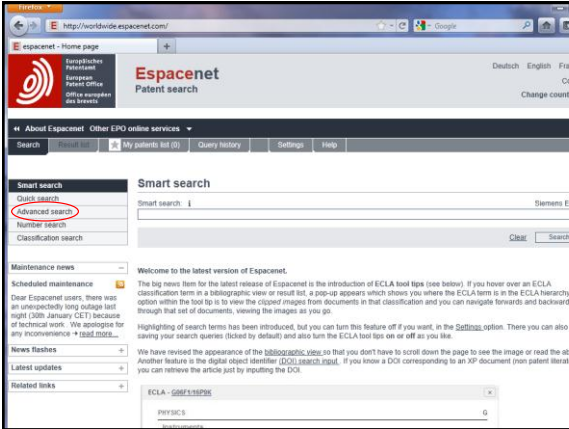


Changes in a given technology over time

Trace the development of a given technology over time by looking at the 'prior art' and 'cited by' links on the front page of U.S. patents.







esepacenet - Bibliographic data

Search + Results + US2008157235 (A1)

Bibliographic data: US2008157235 (A1) — 2008-07-03

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CONTROLLED BUCKLING STRUCTURES IN SEMICONDUCTOR INTERCONNECTS AND NANOMEMBRANES FOR STRETCHABLE ELECTRONICS

Page bookmark US2008157235 (A1) - CONTROLLED BUCKLING STRUCTURES IN SEMICONDUCTOR INTERCONNECTS AND NANOMEMBRANES FOR STRETCHABLE ELECTRONICS

Inventors: ROGERS JOHN A (US); MEITL MATTHEW (US); SUN YUGANG (US); KO HELING CHO (US); CARLSON ANDREW (US); CHO YOUNG MOON (US); STOKOVICH MARK (US); JIANG HANGKONG (US); HUANG YONGSIANG (US) ±

Applicants:

Classification:
 - International: H01L21/00; H01L29/64; H05K1/00
 - European: H01L21/028; H05K1/028

Application number: US20070851182 20070906

Priority numbers: US20070851182 20070906; US20060145574 20060602; US20050145542 20050602; US20060423287 20060609; US20070442629 20070918; US2005024883P 20050906; US2004057077P 20040604; US2004091161P 20040801; US2006065936P 20060604; US2006066139P 20060618; US20060827017P 20060604; US2006079014P 20060407

Abstract of US2008157235 (A1)

In an aspect, the present invention provides stretchable, and optionally printable, components such as semiconductor and electronic circuit capable of providing



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Search + Results + US2008157235 (A1) → Family page 1

Family list: US2008157235 (A1) — 2008-07-03

Approximate (8) applications for: US2008157235 (A1)

Sort by Priority date Sort order Descending

1. Methods and devices for fabricating and assembling printable semiconductor elements

Inventor	Applicant	EC:	IPC:	Publication info:	Priority date:
★ ETIENNE MENARD (US) UNIV ILLINOIS JAE LEE KEON (US) (+3)	UNIV ILLINOIS (US)	8821300 H01L21/02436 (+4)	B81C1/00 H01L21/00 H01L21/09 (+1)	CN101126433 (A) 2008-02-08 CN10120433 (B) 2010-12-08	2004-06-04

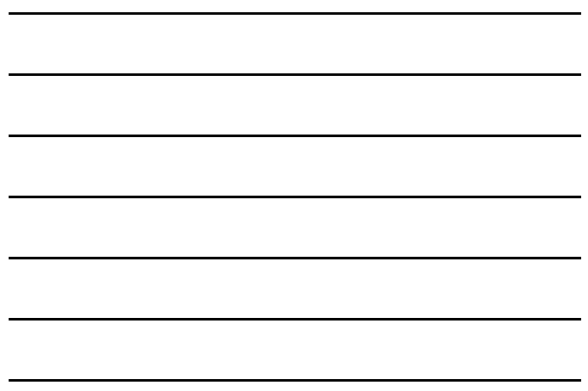
2. Printable semiconductor structures and related methods of making and assembling

Inventor	Applicant	EC:	IPC:	Publication info:	Priority date:
★ NUZZO RALPH G (US) ROGERS JOHN A (+8)	UNIV ILLINOIS (US)	8821300	H01L21/02	CN10121056 (A) 2010-01-28	2005-06-02

3. Controlled buckling structures in semiconductor interconnects and nanomembranes for stretchable electronics

Inventor	Applicant	EC:	IPC:	Publication info:	Priority date:
★ ROGERS JOHN A (US) MATTHEW MEITL (+1)	UNIV ILLINOIS (US)	B81C1/00522 8821300 (+1)	H01B7/06 H01B3/00 (+1)	CN101817195 (A) 2010-03-24	2006-09-08

4. Methods and devices for fabricating and assembling printable semiconductor elements



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5. METHODS AND DEVICES FOR FABRICATING AND ASSEMBLING PRINTABLE SEMICONDUCTOR ELEMENTS

Inventor	Applicant	EC:	IPC:	Publication info:	Priority date:
★ NUZZO RALPH G (US) ROGERS JOHN A (US) (+5)	UNIV ILLINOIS (US)	8821300 H01L21/02436 (+4)	B81C1/00 H01L21/02 H01L21/09 (+1)	EP1758422 (A2) 2007-03-07 EP1758422 (A4) 2011-04-06	2004-06-04

7. PRINTABLE SEMICONDUCTOR STRUCTURES AND RELATED METHODS OF MAKING AND ASSEMBLING

Inventor	Applicant	EC:	IPC:	Publication info:	Priority date:
★ NUZZO RALPH G (US) ROGERS JOHN A (US) (+8)	UNIV ILLINOIS (US)	8821300 H01L21/0280 (+5)	H01L21/02 H01L18/04 H01L23/62 (+3)	EP1915774 (A2) 2008-04-30	2005-06-02

8. CONTROLLED BUCKLING STRUCTURES IN SEMICONDUCTOR INTERCONNECTS AND NANOMEMBRANES FOR STRETCHABLE ELECTRONICS

Inventor	Applicant	EC:	IPC:	Publication info:	Priority date:
★ ROGERS JOHN A (US) MEITL MATTHEW (US) (+1)	UNIV ILLINOIS (US)	B81C1/00522 8821300 (+1)	H01B7/06 H01B3/00 (+1)	EP2064710 (A2) 2009-06-03 EP2064710 (A4) 2011-05-04	2006-09-08

9. PATTERN TRANSFER PRINTING BY DYNAMIC CONTROL OF ADHESION ON ELASTOMER STAMP

Inventor	Applicant	EC:	IPC:	Publication info:	Priority date:
★ NUZZO RALPH G (US) ROGERS JOHN A (+8)	UNIV ILLINOIS (US)	8821300	H01L21/02 H01L21/12	JP2007307693 (A) 2007-02-01	2005-06-02

10. STRETCHABLE SINGLE CRYSTAL SILICON FOR HIGH PERFORMANCE ELECTRONICS ON RUBBER SUBSTRATE

Inventor	Applicant	EC:	IPC:	Publication info:	Priority date:
★ ROGERS JOHN A (US) KUHANO DAHL-YOUNG (+1)	UNIV ILLINOIS (US)	H01L21/00 H01L28/785 (+1)	H01L21/02 H01L21/339 H01L21/12 (+1)	EP2007281 (A4) 2007-10-25	2006-04-07

11. METHODS AND DEVICES FOR FABRICATING AND ASSEMBLING PRINTABLE SEMICONDUCTOR ELEMENTS



Espacenet - INPADOC patent family

11. METHODS AND DEVICES FOR FABRICATING AND ASSEMBLING PRINTABLE SEMICONDUCTOR ELEMENTS

Inventor:	Applicant:	EC:	IPC:	Publication info:	Priority date:
		B021100 H01L182K4L3L5 (+4)	B81C100 H01L2102 H01L2129 (+13)	JP2005015511 (A) 2005-01-24 H01L2102	2004-05-04

12. PRINTABLE SEMICONDUCTOR STRUCTURES AND RELATED METHODS OF MAKING AND ASSEMBLING

Inventor:	Applicant:	EC:	IPC:	Publication info:	Priority date:
		B021100 H01L2108 H01L2138 (+5)	B81B300 H01L2108 H01L2138 (+5)	JP200508322 (A) 2005-01-28 H01L2108	2005-06-02

13. CONTROLLED BUCKLING STRUCTURES IN SEMICONDUCTOR INTERCONNECTS AND NANOMEMBRANES FOR STRETCHABLE ELECTRONICS

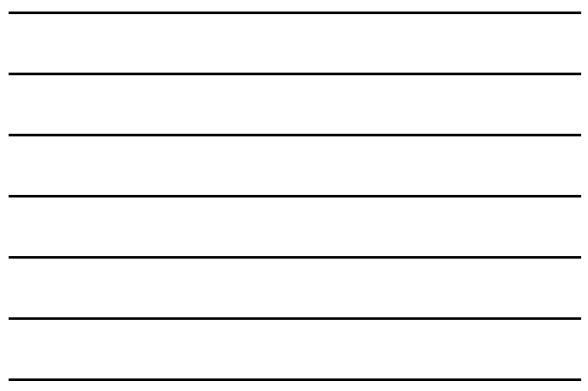
Inventor:	Applicant:	EC:	IPC:	Publication info:	Priority date:
		B01B300032 B021100 (+11)	B81B300 H01L2108 H01L2108 (+11)	JP200501238 (A) 2005-01-28 H01L2108	2005-00-06

14. PATTERN TRANSFER PRINTING BY KINETIC CONTROL OF ADHESION TO AN ELASTOMERIC STAMP

Inventor:	Applicant:	EC:	IPC:	Publication info:	Priority date:
MUZZO RALPH G (US) ROGERS JOHN A (US) (+6)	UNIV ILLINOIS (US) (US)	B021100 H01L213300 (+5)	B81C100 H01L1807 (+5)	602060175005 (A) 2005-12-26 6010706431 (B1) 2008-01-28	2005-09-02

15. METHODS AND DEVICES FOR FABRICATING AND ASSEMBLING PRINTABLE SEMICONDUCTOR ELEMENTS

Inventor:	Applicant:	EC:	IPC:	Publication info:	Priority date:
MUZZO RALPH G (US) ROGERS JOHN A (US) (+6)	UNIV ILLINOIS (US) (US)	B021100 H01L182K4L3L5 (+4)	B81C100 H01L2107 H01L2112 (+6)	60206017017484 (A) 2007-04-04 H01L2112	2004-06-04



Espacenet - Bibliographic data

Search → Results → US2008157235 (A1)

Bibliographic data: US2008157235 (A1) — 2008-07-03

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CONTROLLED BUCKLING STRUCTURES IN SEMICONDUCTOR INTERCONNECTS AND NANOMEMBRANES FOR STRETCHABLE ELECTRONICS

Page bookmark US2008157235 (A1) - CONTROLLED BUCKLING STRUCTURES IN SEMICONDUCTOR INTERCONNECTS AND NANOMEMBRANES FOR STRETCHABLE ELECTRONICS

Inventor(s): ROGERS JOHN A (US); MITL MATTHEW (US); SUN YUGANG (US); KO HEUNG CHO (US); CARLSON ANDREW (US); CHOI WON MOOK (US); STOKROVICH MARK (US); HANG HANGZONG (US); HUANG YONGSIANG (US) ;

Applicant: INPADOC patent family

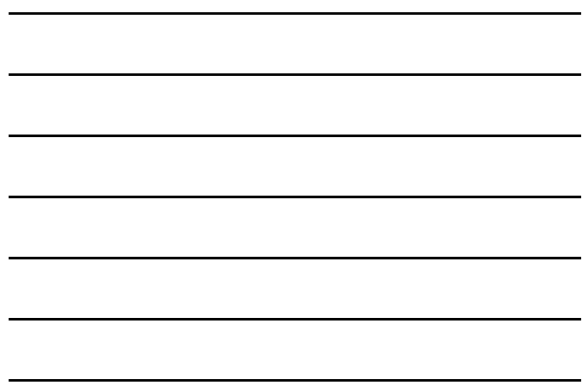
Classification: International: H01L2108; H01L2694; H05K100
European: H01L2108; H05K1020

Application number: US2007051182 20070906

Priority numbers: US2007081182 20070906; US2005014674 20050602; US2005014742 20050902; US2006042328 20060609; US2007094426P 20070916; US200602483P 20060906; US2004057077P 20040904; US2004001091P 20040811; US2005050630P 20050204; US2005096339P 20050316; US2005087617P 20050504; US2006076194P 20060407

Abstract of US2008157235 (A1)

In an aspect, the present invention provides stretchable, and optionally printable, components such as semiconductors and electronic circuits capable of providing good performance when subjected to mechanical stress and/or deformation.



Espacenet - INPADOC legal status

Search → Results → US2008157235 (A1) → Family page 1 → US2008157235 (A1)

INPADOC legal status: US2008157235 (A1) — 2008-07-03

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CONTROLLED BUCKLING STRUCTURES IN SEMICONDUCTOR INTERCONNECTS AND NANOMEMBRANES FOR STRETCHABLE ELECTRONICS

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Legal status of US2008157235 (A1), 2008-07-03

US F 8518207 A (Patent of invention)

PKS Date: 2008/03/24

PKS Code: AS

Code Expl: ASSIGNMENT

NEW OWNER: THE BOARD OF TRUSTEES OF THE UNIVERSITY OF ILLINOIS

FURTHER INFORMATION: ASSIGNMENT OF ASSIGNORS INTEREST ASSIGNORS ROGERS, JOHN A, MITL, MATTHEW, SUN, YUGANG AND OTHERS REEL/FRAME 020891/0972, SIGNING DATES FROM 2007108 TO 20080208



US2008157235 (A1)

Bibliographic data: US2008157235 (A1) — 2008-07-03

CONTROLLED BUCKLING STRUCTURES IN SEMICONDUCTOR INTERCONNECTS AND NANOMEMBRANES FOR STRETCHABLE ELECTRONICS

Page bookmark US2008157235 (A1) — CONTROLLED BUCKLING STRUCTURES IN SEMICONDUCTOR INTERCONNECTS AND NANOMEMBRANES FOR STRETCHABLE ELECTRONICS

Inventors: ROGERS JOHN A (US); MEHL MATTHEW (US); SUN YUGANG (US); KO HELING CHO (US); CARLSON ANDREW (US); CHO YOUN MOOK (US); STOKOVICH MARK (US); JIANG HANGING (US); HUANG YONGDANG (US) ±

Applicants:

Classification:

- International: H01L21/00; H01L29/04; H05K1/00
- European: H01L21/028; H05K1/028

Application number: US20070851182 20070906

Priority numbers: US20071851182 20070906; US20059145574 20050602; US2005014542 20050602; US20060423287 20060609; US20070442629 20070918; US2005024833P 20050906; US2004057077P 20040604; US20040911661P 20040801; US2006065059P 20060604; US2006066139P 20060718; US2006087671P 20060604; US2006079014P 20060407

Abstract of US2008157235 (A1)

In an aspect, the present invention provides stretchable, and optionally printable, components such as semiconductor and electronic circuit capable of providing



US2008157235 (A1)

LIST OF CITING DOCUMENTS: US2008157235 (A1) — 2008-07-03

8 documents citing US2008157235 (A1)

Sort by: Priority date | Sort order: Descending | Set

1. ELECTROPHYSIOLOGY IN VIVO USING CONFORMAL ELECTRONICS

Inventor: SEKARIC LOJKA (US); XIM DAE-HYEON (US)

Applicant: UNIV ILLINOIS (US); UNIV PENNSYLVANIA (US)

EC: A61B5/072; A61B5/0502

IPC: A61B5/00; A61B5/05; A61B12/00

Publication info: US2011048490 (A1)

Priority date: 2009-12-18; 2011-07-14

2. SEMICONDUCTOR NANOWIRE WITH BUILT-IN STRESS

Inventor: SEKARIC LOJKA (US); CHEN XI (US); DURESETI (US) (+1)

Applicant: IBM (US)

EC: B82Y3300

IPC: H01L21/338; H01L21/3350

Publication info: US2011048490 (A1)

Priority date: 2009-04-03; 2011-05-05; US7989233 (B2); 2011-08-02

3. SYSTEMS AND METHODS FOR SELF-ASSEMBLING ORDERED THREE-DIMENSIONAL PATTERNS BY BUCKLING OF THIN FILMS BOUNDED TO CURVED SUBSTRATE SURFACES

Inventor: CHEN XI (US)

Applicant: UNIV COLUMBIA (US); CHEN XI (US)

EC: H01L21/338

IPC: H01L29/12; H01L29/14

Publication info: W02011050181 (A1)

Priority date: 2009-10-21; 2011-04-29

4. METHOD FOR GENERATING AN ELECTRONIC SYSTEM, METHOD FOR GENERATING A FREEFORM SURFACE HAVING SUCH A SYSTEM, AND ELECTRONIC SYSTEM AND FREEFORM SURFACE HAVING SUCH A SYSTEM

Inventor: LOEHER THOMAS (DE); OSTMANN ANDREAS (DE) (+1)

Applicant: FRANKHOFFER GES FORSCHUNG (DE); UNIV BERLIN TECH (DE) (+3)

EC: H05K3/280

IPC: H05K3/28

Publication info: W02011000550 (A1)

Priority date: 2009-06-29; 2011-01-06



US2008157235 (A1)

4. METHOD FOR GENERATING AN ELECTRONIC SYSTEM, METHOD FOR GENERATING A FREEFORM SURFACE HAVING SUCH A SYSTEM, AND ELECTRONIC SYSTEM AND FREEFORM SURFACE HAVING SUCH A SYSTEM

Inventor: LOEHER THOMAS (DE); OSTMANN ANDREAS (DE) (+1)

Applicant: FRANKHOFFER GES FORSCHUNG (DE); UNIV BERLIN TECH (DE) (+3)

EC: H05K3/280

IPC: H05K3/28

Publication info: W02011000550 (A1)

Priority date: 2009-06-29; 2011-01-06

5. MASKLESS PROCESS FOR SUSPENDING AND THINNING NANOWIRES

Inventor: SANGSARANUP SARUNYA (US); COHEN DUTY (US) (+1)

Applicant: IBM (US); SANDSARANUP SARUNYA (US) (+1)

EC: B82Y3300

IPC: H01L29/18; H01L21/3350

Publication info: W0201090078 (A1)

Priority date: 2008-02-04; 2010-08-12

6. INDIVIDUALLY ADDRESSABLE NANO-SCALE MECHANICAL ACTUATORS

Inventor: YANG JIANHUA (US); WILLIAMS R STANLEY (US) (+1)

Applicant: HEWLETT PACKARD DEVELOPMENT CO (US)

EC: B81B7/00

IPC: B81B7/00; B81B7/04

Publication info: W02010902443 (A1)

Priority date: 2008-06-26; 2011-01-05

7. Resistor Composite Stamp by Dry Transfer Printing of Semiconductor Elements

Inventor: MENARD ETIENNE (US)

Applicant: SEMPRUS, INC

EC: B82Y3300; G07F3/00

IPC: B41K1/42

Publication info: US201018420 (A1)

Priority date: 2010-01-20; US7927878 (B2); 2011-06-19

8. RELEASE STRATEGIES FOR MAKING TRANSFERABLE SEMICONDUCTOR STRUCTURES, DEVICES AND DEVICE COMPONENTS

Inventor: ROGERS JOHN A (US); NUZZO RALPH G (US) (+5)

Applicant: THE BOARD OF TRUSTEES OF THE UNIVERSITY OF ILLINOIS

EC: B81C1/00F2F; H01L21/382

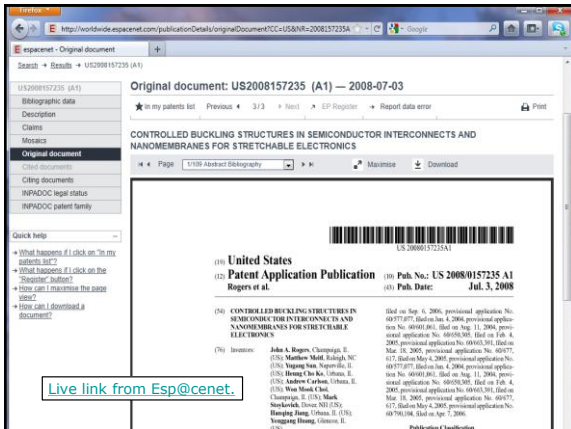
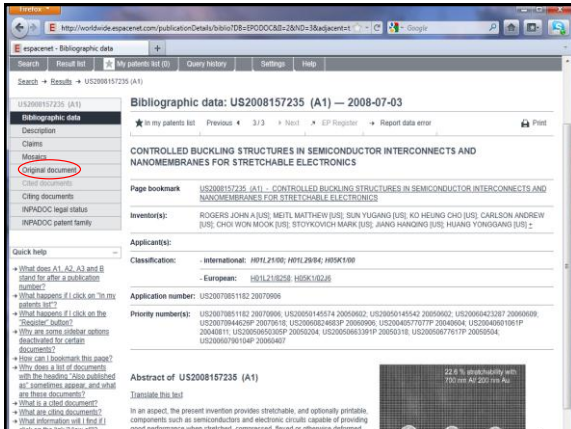
IPC: H01L21/30; H01L31/18

Publication info: US2008188171 (A1)

Priority date: 2006-05-08; US7921233 (B2); 2011-04-26

9. A heterostructure structure of zinc oxide based nanowire and semiconductor thin film, preparation thereof, and nano-device comprising same





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<http://patft.uspto.gov/help/images.htm>
Good source for a (free!) recommended TIFF-file viewer software named [Alternatiff](#) for Windows machines only.

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<http://campusguides.lib.utah.edu/content.php?pid=71473>
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<http://law.unh.edu/thomasfield/ipbasics/index.php>

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Thanks!